

Confirmation No. 6633

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	LETAVIC	Examiner:	Kebede, B.
Serial No.:	10/560,573	Group Art Unit:	2823
Filed:	December 12, 2005	Docket No.:	US030162US2
Title:	High-Side CMOS in an Integrated SOI Process		

RESPONSE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Customer No.
65913

Dear Sir:

In response to the non final office action dated March 20, 2008, please reconsider the application in view of the following amendments and remarks.

A complete listing of the claims begins on page 2 of this paper.

Remarks/Arguments follow on page 4.

**Authorization is provided to charge/credit Deposit Account 50-0996
(NXPS.246 PA) any requisite fees/overages to enter this paper.**